**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE THE

APPLICATION OF:

Priyadarsan Patra, et al.

CONFIRMATION NO.:

5814

APPLICATION NO.:

10/665,521

ART UNIT:

2825

FILED:

September 22, 2003

EXAMINER:

Thuan V. Do

TITLE:

TIME SYNTHESIS FOR POWER

OPTIMIZATION OF HIGH PERFORMANCE CIRCUITS

ASSIGNEE:

INTEL CORPORATION

## RESPONSE TO OFFICE ACTION

MAIL STOP AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In response to the Office Action having a mailing date of January 18, 2006 ("Office Action"), please reconsider this patent application in view of the following amendments and remarks.

Applicant respectfully petitions for a one month extension of time to extend the period for reply to May 18, 2006.

I, Cayle Betish, hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 18, 2006.

(Signature of person mailing correspondence)